

WHAT IS CLAIMED IS:

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1. An image processing unit, comprising:
a frequency dividing device that divides a frequency of a drive clock for an imaging device;
an A/D converting device that converts an image signal outputted from said imaging device according to said drive clock into a digital image data; and
a signal processing part that captures said digital image data outputted from said A/D converting device in synchronization with a frequency-divided clock outputted from said frequency dividing device, and that processes said digital image data.
 2. The image processing unit as set forth in claim 1, wherein said imaging device is a CCD solid state imaging device whose pixels are arranged in one of G-stripe arrangement and Bayer arrangement.
 3. The image processing unit as set forth in claim 1, wherein said frequency dividing device divides the frequency of said drive clock by an odd number.
 4. The image processing unit as set forth in claim 2, wherein said frequency dividing device divides the frequency of said drive clock by an odd number.
 5. The image processing unit as set forth in claim 1, further comprising a display that displays an image according to an image data outputted from said signal processing part.
 6. An image processing unit, comprising:
an imaging device;
a timing generating device that generates a drive clock for driving said

